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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/527,820

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Yoshihiko Takagi

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7590

09/23/2008

NATH & ASSOCIATES
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Alexandria, VA 22314

EXAMINER

CERVETTI, DAVID GARCIA

ART UNIT

PAPER NUMBER

2136

MAIL DATE

DELIVERY MODE

09/23/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/527,820	Applicant(s) TAKAGI ET AL.	
	Examiner David García Cervetti	Art Unit 2136	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 June 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 20-34 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 20-34 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 March 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>5/9/08</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Applicant's arguments filed Jun 23, 2008, have been fully considered.
2. Claims 20-34 are pending and have been examined. Claims 1-19 have been cancelled.

Response to Amendment

3. Applicant's arguments with respect to the prior art have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

4. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
5. **Claims 20-34 are rejected under 35 U.S.C. 102(b) as being anticipated by Quere (US 2003/0131250).**

Regarding claims 20 and 31, Quere teaches

an access method for an apparatus to gain access to a specific area of a memory device, said specific area being assigned an area number, the method comprising the steps of (abstract):

first transmitting designation information from the apparatus to the memory device, said designation information designating an access area in the memory device, and including the area number of the access area, an access start offset in the specific area associated with the area number and a size of data to be accessed (par.57-59, initialize prior to command with addresses);

second transmitting verification information generated based on the designation information, together with a processing command / verification data obtained by encrypting the designation information using the verification key, together with a processing command, from the apparatus to the memory device, after first transmitting the designation information (par. 64-77, command field w/ cryptographic information);

verifying the verification information using the designation information / and the verification key at the memory device (par. 63-77, command field w/ cryptographic information, hash, signature); and

executing the processing command when the verification succeeds at the memory device (par. 63-77, verification of signature data);

Regarding claim 21, Quere teaches

an access method for an apparatus to gain access to a specific area of a memory device, said specific area being assigned an area number, the method comprising the steps of (abstract, par 58):

sharing, by the apparatus, a verification key with the memory device (par. 63-77, cryptographic information);

first transmitting designation information from the apparatus to the memory device, said designation information designating an access area in the memory device, and including the area number of the access area, an access start offset in the specific area associated with the area number and a size of data to be accessed (par.57-59, initialize prior to command with addresses);

second transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command, from the apparatus to the memory device, after first transmitting the designation information (par. 64-77, command field w/ cryptographic information);

verifying the verification data using the designation information and the verification key at the memory device (par. 63-77, command field w/ cryptographic information, hash, signature); and

executing the processing command when the verification succeeds at the memory device (par. 63-77, verification of signature data);

Regarding claims 22 and 32, Quere teaches

an access method for an apparatus to gain access to a specific area of a memory device, the method comprising the steps of (abstract):

sharing, by the apparatus, a verification key with the memory device (par. 63-77, cryptographic information);

requesting, by the apparatus, transmission of random numbers to the memory device; transmitting, by the memory device, random numbers to the apparatus (par. 63-77, cryptographic information, fig.2b);

first transmitting designation information from the apparatus to the memory device, said designation information designating an access area in the memory device, and including the area number of the access area, an access start offset in the specific area associated with the area number and a size of data to be accessed (par.57-59, initialize prior to command with addresses);

second transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command, from the apparatus to the memory device, after first transmitting the designation information (par. 64-77, command field w/ cryptographic information);

verifying the verification data using the random numbers, the designation information and the verification key at the memory device (par. 63-77, command field w/ cryptographic information, hash, signature); and

executing the processing command when the verification succeeds at the memory device (par. 63-77, verification of signature data);

Regarding claim 23, Quere teaches

a memory device whose data of a specific area is read and written by an apparatus, said specific area being assigned an area number, the memory device comprising (abstract);

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write (par.57-59, initialize prior to command with addresses);

a designation information verifier that performs verification processing on the verification information using the designation information (par. 63-77, command field w/ cryptographic information);

a storage area that stores data (par.91-92, memory);

a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds (par.138-145, I/O operations and crypto operations to protect data/command);

a data transmitter that transmits data read by the storage area access section to the apparatus (par.90-92, interface); and

a data receiver that receives data to write from the apparatus (par.90-92, interface).

Regarding claim 27, Quere teaches

an information apparatus that reads and writes data to a specific area from/in a memory device, said specific area being assigned an area number, the information apparatus comprising (abstract):

a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area (par.57-59, initialize prior to command with addresses);

a verification information generator that performs processing for generating verification information based on the designation information (par. 63-77, command field w/ cryptographic information, hash, signature);

a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a processing command for reading or writing data (par.90-92, interface, par.138-145, I/O operations and crypto operations to protect data/command);

a data transmitter that transmits data to the memory device when the processing command is write (par.90-92, interface); and

a data receiver that receives data from the memory device when the processing command is read (par.90-92, interface).

Regarding claims 24 and 28, Quere teaches wherein, the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to the apparatus (par. 63-77, cryptographic information, fig.2b).

Regarding claims 25 and 29, Quere teaches wherein the designation information verifier performs the verification processing using the verification information and a verification key (par. 63-77, command field w/ cryptographic information, hash, signature).

Regarding claims 26 and 30, Quere teaches a verification key sharing section that shares the verification key with the apparatus (par. 63-77, command field w/ cryptographic information, hash, signature).

Regarding claim 33, Quere teaches wherein the specific area is a security protection area to which an access from an outer apparatus is restricted (par.41-42, key memory).

Regarding claim 34, Quere teaches wherein the processing command is a command for reading out or writing a data in the specific area (par.138-145, I/O operations and crypto operations to protect data/command).

Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

7. A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David García Cervetti whose telephone number is (571)272-5861. The examiner can normally be reached on Monday-Tuesday and Thursday-Friday.

9. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nasser Moazzami can be reached on (571)272-4195. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

10. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/David García Cervetti/
Examiner, Art Unit 2136

/Nasser G Moazzami/
Supervisory Patent Examiner, Art Unit 2136